Basic Bringup of PLL-Evalboard-Synthesizer

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# Introduction

This document describes the bring up of our Phase-Locked Loop (PLL) frequency synthesizer for generating medium-precision RF carrier wave (CW) signals. The Quantum Ion Trap group at the Institute for Quantum Computing (IQC) uses a series of evaluation boards from Analog Devices and Perigrine Semiconductor to provide a low-cost series of RF drive signals.

# Initial Bring Up of ADF41020 Evaluation Board

The evaluation board is shown below:

# Bring up of Raspberry Pi

The Raspberry Pi is shown below: